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CENTRAL FAX CENTER

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In re Patent Application of
MARINET ET AL.
Serial No. 09/995,258
Filed: **NOVEMBER 27, 2001**

In the Specification:

Please replace the paragraph beginning at page 9,
lines 18-34, with the following rewritten paragraph:

The drain-source current includes a random component when the channel dimensions are chosen to be close to the minimum resolution allowed by the manufacturing technology in use. The dimensions may even be slightly less than this minimum because defects may otherwise appear when developing the channel implantation mask. Therefore, as shown in the enlarged view in ~~[[FIG. 3b]]~~ FIG. 3a, when the channel 24 includes dimensions and a folding step that are close to the limits allowed by the technology in use, or even slightly smaller than these limits, the folded portions obtained include a rounded shape 24' inside the fold. For example, with a 0.35 μm integration technology, the width of the parallel and folded portions of the gate is less than 1 μm , and preferably approximately 0.7 μm , whereas the distance between the parallel portions is on the order of 1.5 μm .